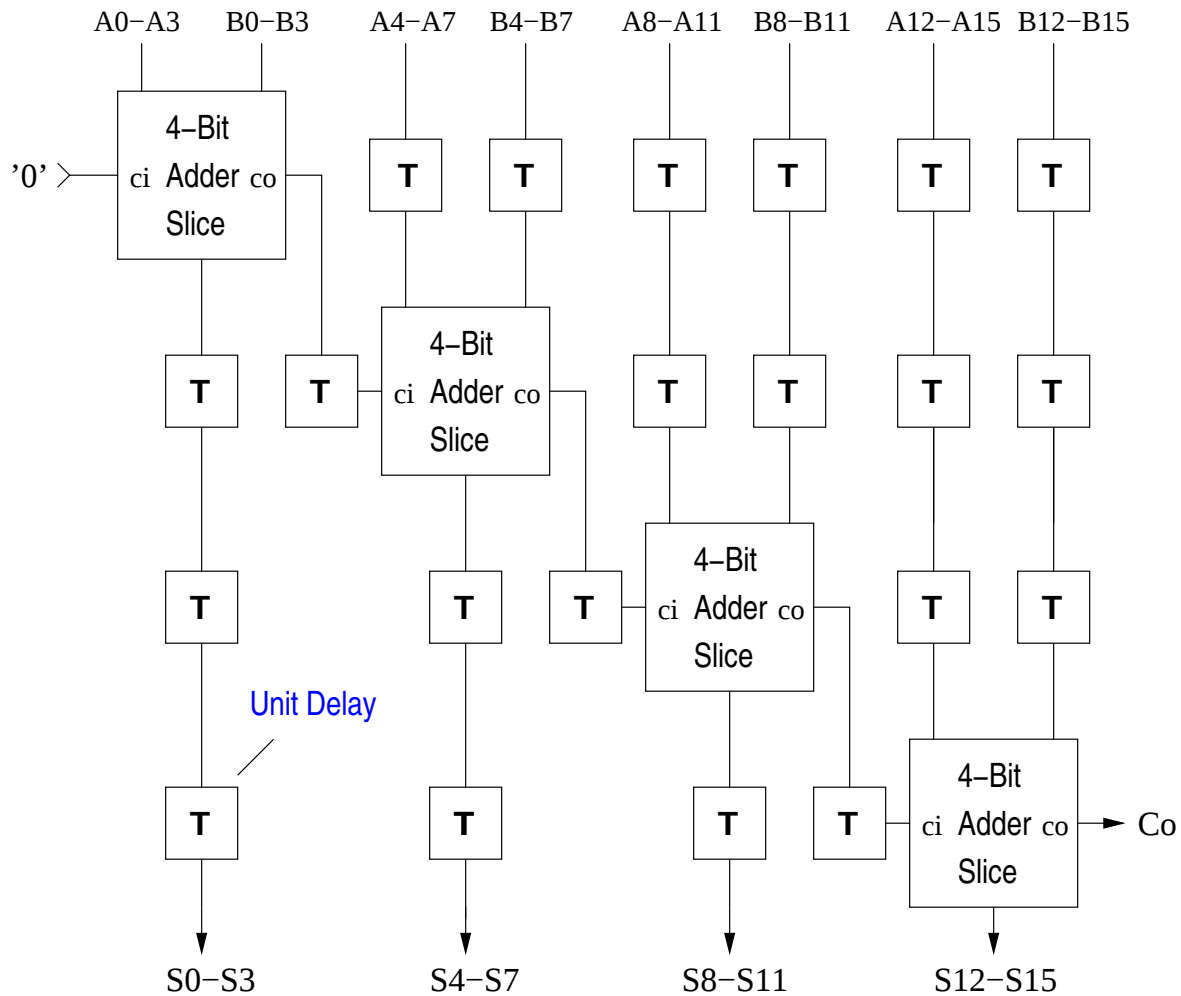


Pipelined Adder

16-Bit Adder using four 4-Bit Slices



Note: Maximum throughput is limited by propagation delay of adder slice only