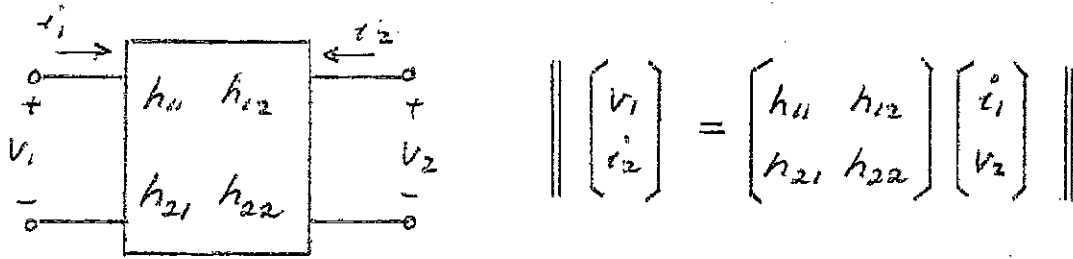
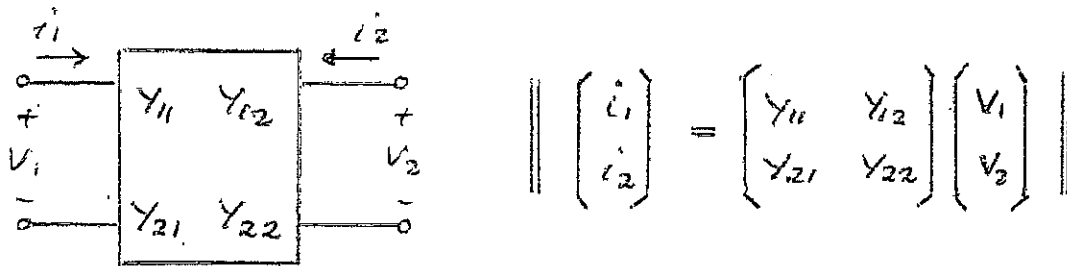


## 2-Port Matrices

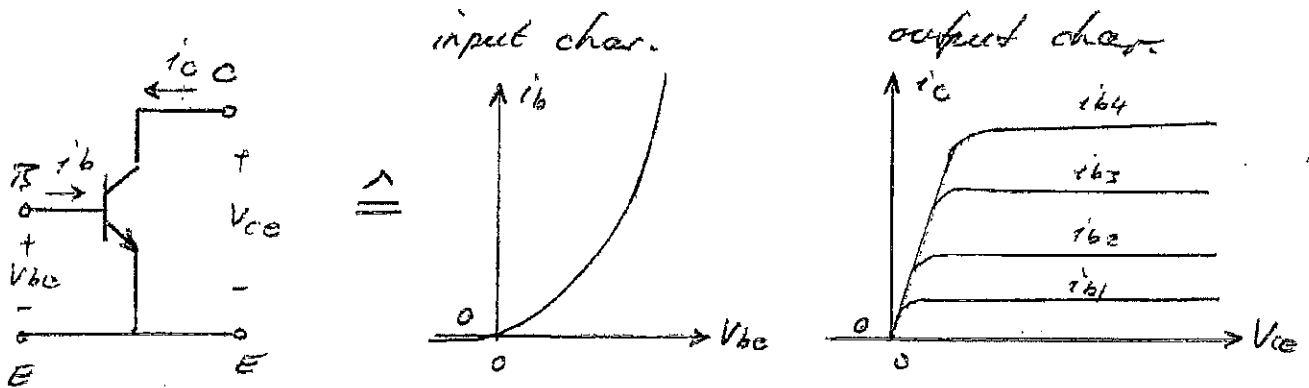
### Def. Hybrid Matrix



### Def. Admittance Matrix

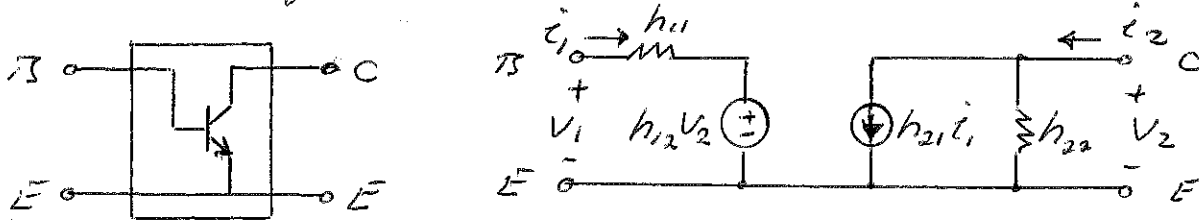


Alternatively, a two port can be described by its input and output  $v-i$  characteristics. For example, a BJT with the emitter as the common terminal possesses the following input and output characteristics:



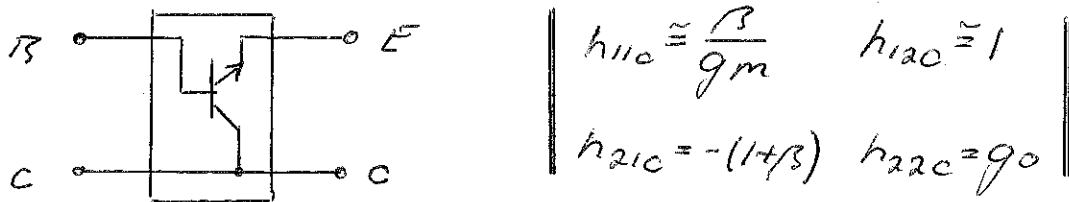
BJT [H]-Parameter Representation

a) CE Configuration



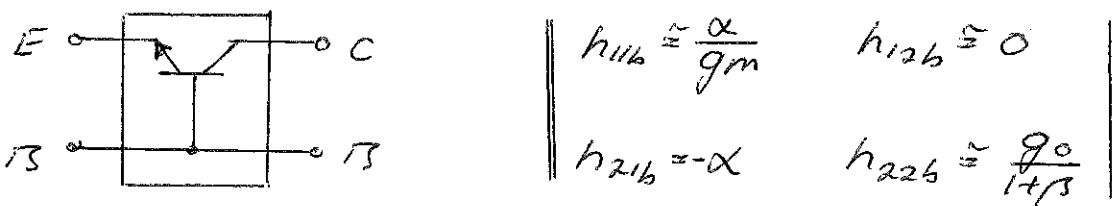
$$\left| \begin{array}{l} g_m = \frac{I_C}{V_T} \\ g_o = \frac{I_C}{V_A} \end{array} \right| \quad \left| \begin{array}{ll} h_{11e} \approx \frac{\beta}{g_m} & h_{12e} \approx 0 \\ h_{21e} = \beta & h_{22e} = g_o \end{array} \right|$$

b) CC Configuration (Emitter Follower)



$$\left| \begin{array}{ll} h_{11c} \approx \frac{\beta}{g_m} & h_{12c} \approx 1 \\ h_{21c} = -(1+\beta) & h_{22c} = g_o \end{array} \right|$$

c) CB Configuration

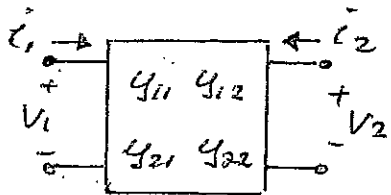


$$\left| \alpha = \frac{\beta}{1+\beta} \right|$$

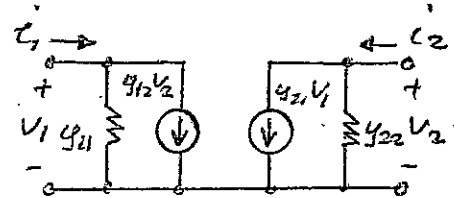
$$\begin{bmatrix} V_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \cdot \begin{bmatrix} i_1 \\ V_2 \end{bmatrix}$$

# FET [Y]-Parameter Representation

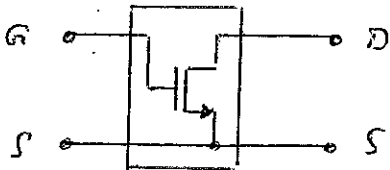
## Definition



$$\begin{cases} i_1 = y_{11} V_1 + y_{12} V_2 \\ i_2 = y_{21} V_1 + y_{22} V_2 \end{cases}$$



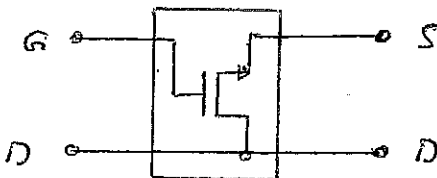
### a) CS Configuration



$$\begin{cases} y_{11s} \approx 0 & y_{12s} \approx 0 \\ y_{21s} = g_m & y_{22s} = g_o \end{cases}$$

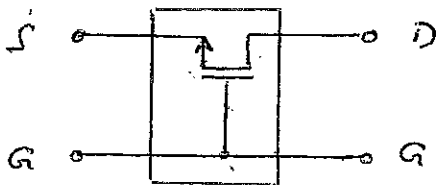
MOSFET  $| g_m = \sqrt{2\mu C_{ox} \frac{W}{L} I_D} ; g_o = \lambda I_D |$

### b) CD Configuration (Source Follower)



$$\begin{cases} y_{11d} \approx 0 & y_{12d} \approx 0 \\ y_{21d} \approx -g_m & y_{22d} \approx g_m + g_o \end{cases}$$

### c) CG Configuration



$$\begin{cases} y_{11g} = g_m + g_o & y_{12g} = -g_o \\ y_{21g} = -g_m - g_o & y_{22g} = g_o \end{cases}$$

Note: typically  $g_m \gg g_o$