

**Homework 6****1) CMOS Opamp DC Analysis**

Solve problem 5.5 & 5.6 in the Johns/Martin text.

**2) CMOS Opamp Ac Analysis**

Solve problems 5.9 & 5.12 in the Johns/Martin text.

**3) High-Gain CMOS Opamp Design**

Design a 2-stage CMOS opamp, which features a dc open-loop gain in excess of **100,000** and a unity-gain frequency of more than **20 MHz**. Assume that the opamp only drives a capacitive load CL of approximately 5pF.