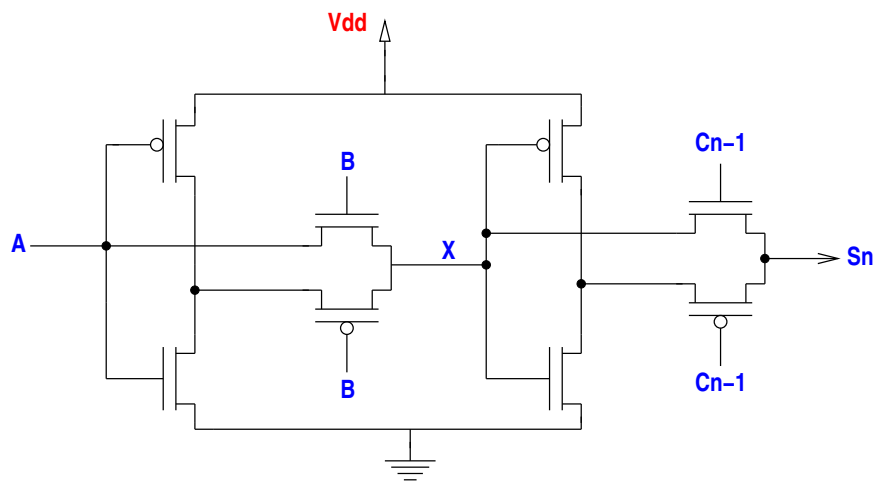
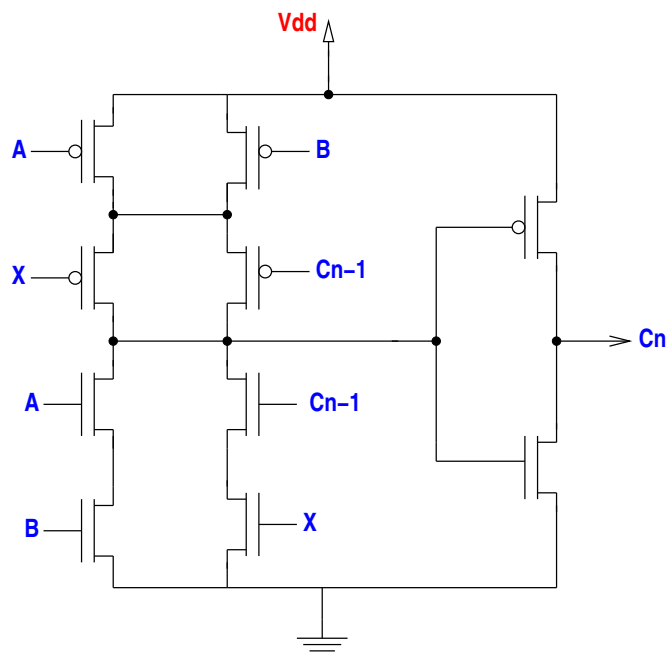


1badd1_50

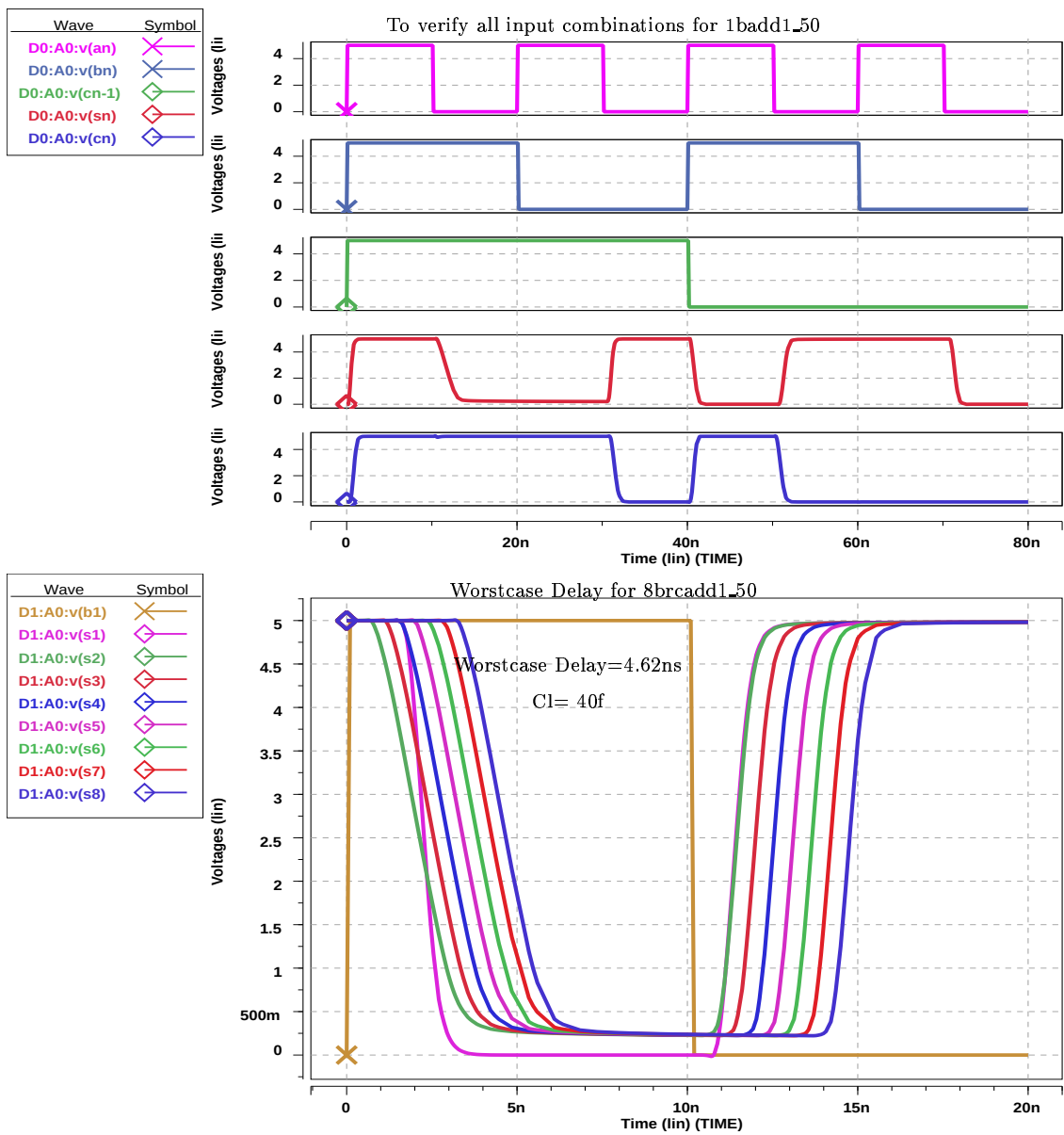
Sum Bit Generator



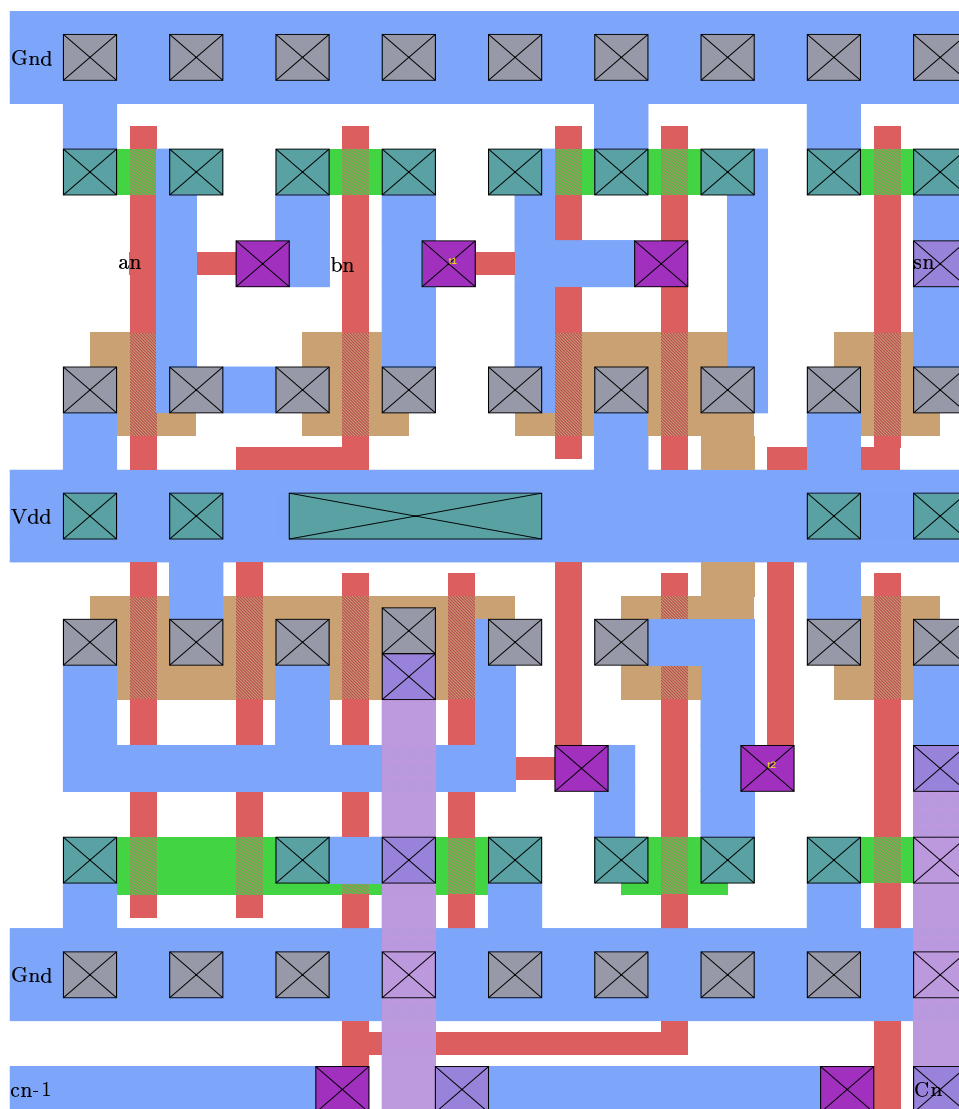
Carry Bit Generator



Schematic for 1-bit CMOS Full Adder



Hspice file created from 1badd1_50.ext and 8brcadd1_50.ext



Magic plot for 1badd1_50