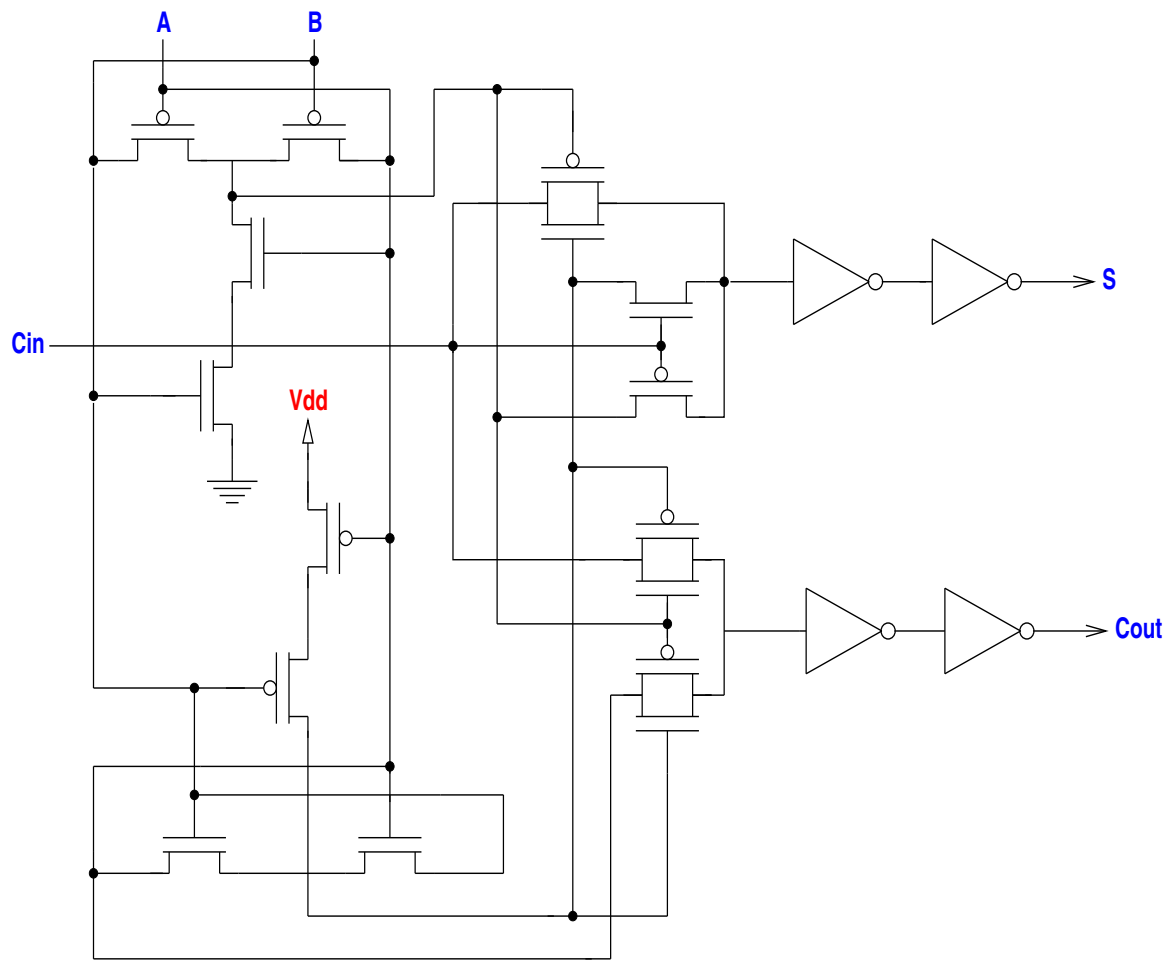
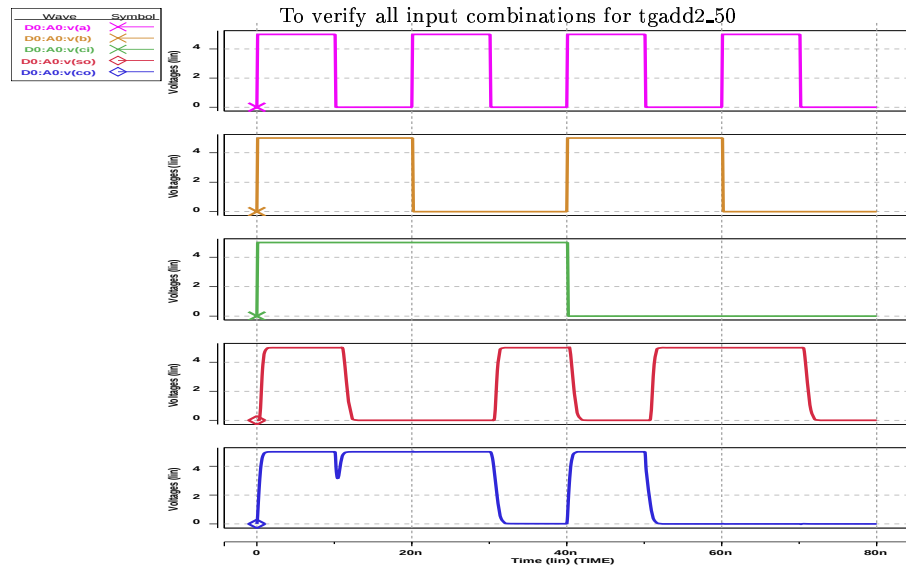


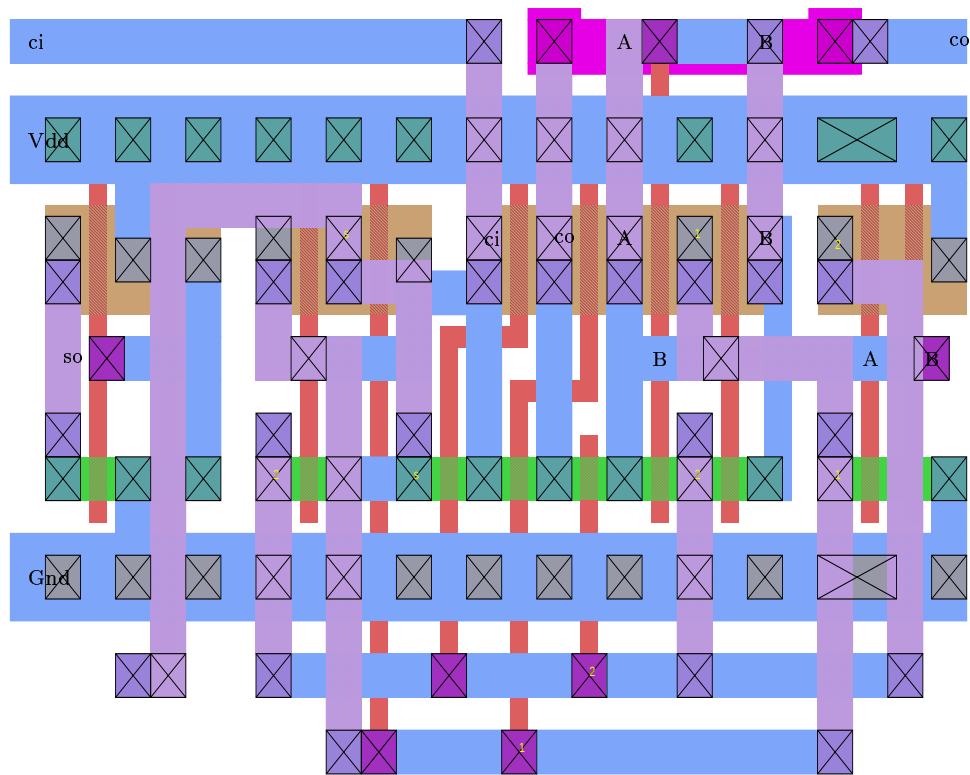
tgadd2_50



Schematic for Transmission Gate Full adder with Outputs buffered



Hspice file created from tgadd2_50



Magic plot for tgadd2_50