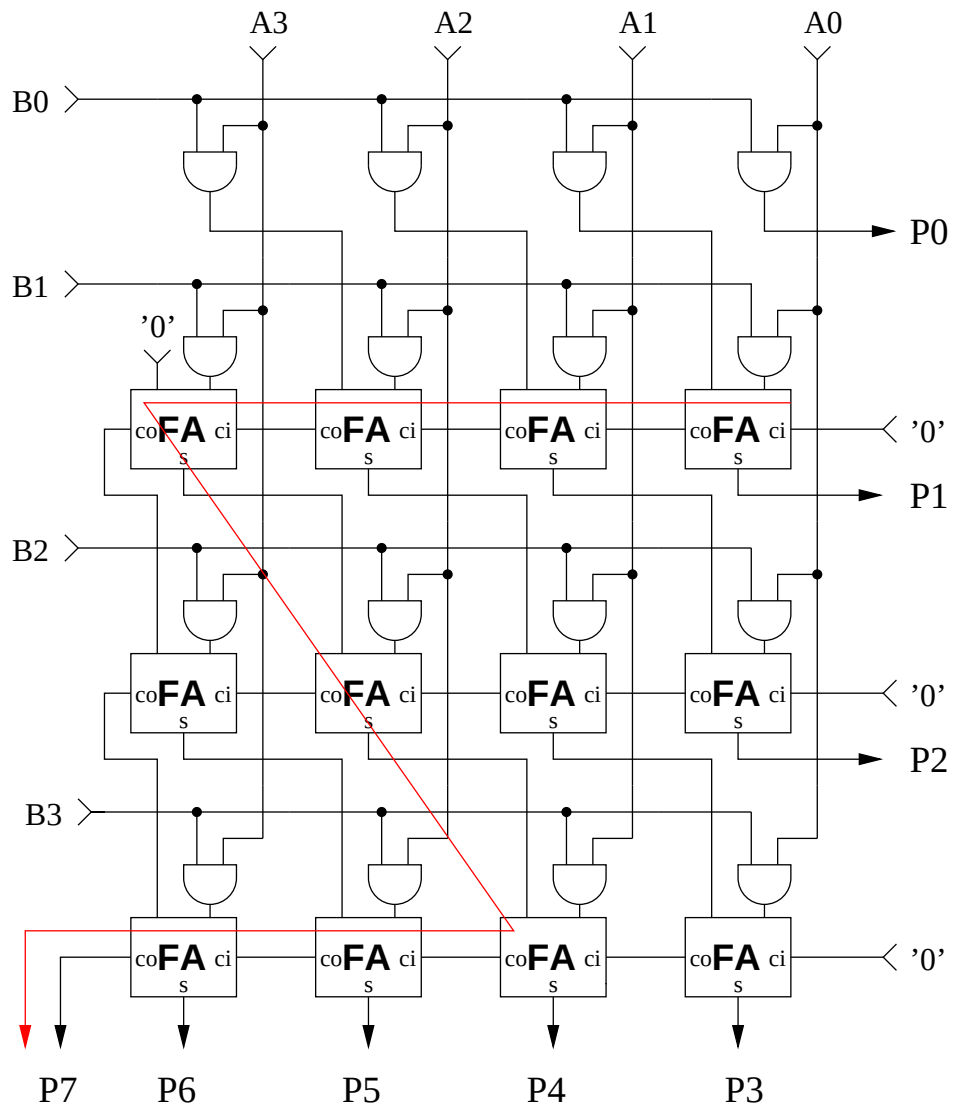


Array Multiplier

Version 1

4x4-Bit Binary Multiplication



Worst Case Propagation Delay: $T_{pd} = [3N-4] \cdot t_{pd}(FA)$