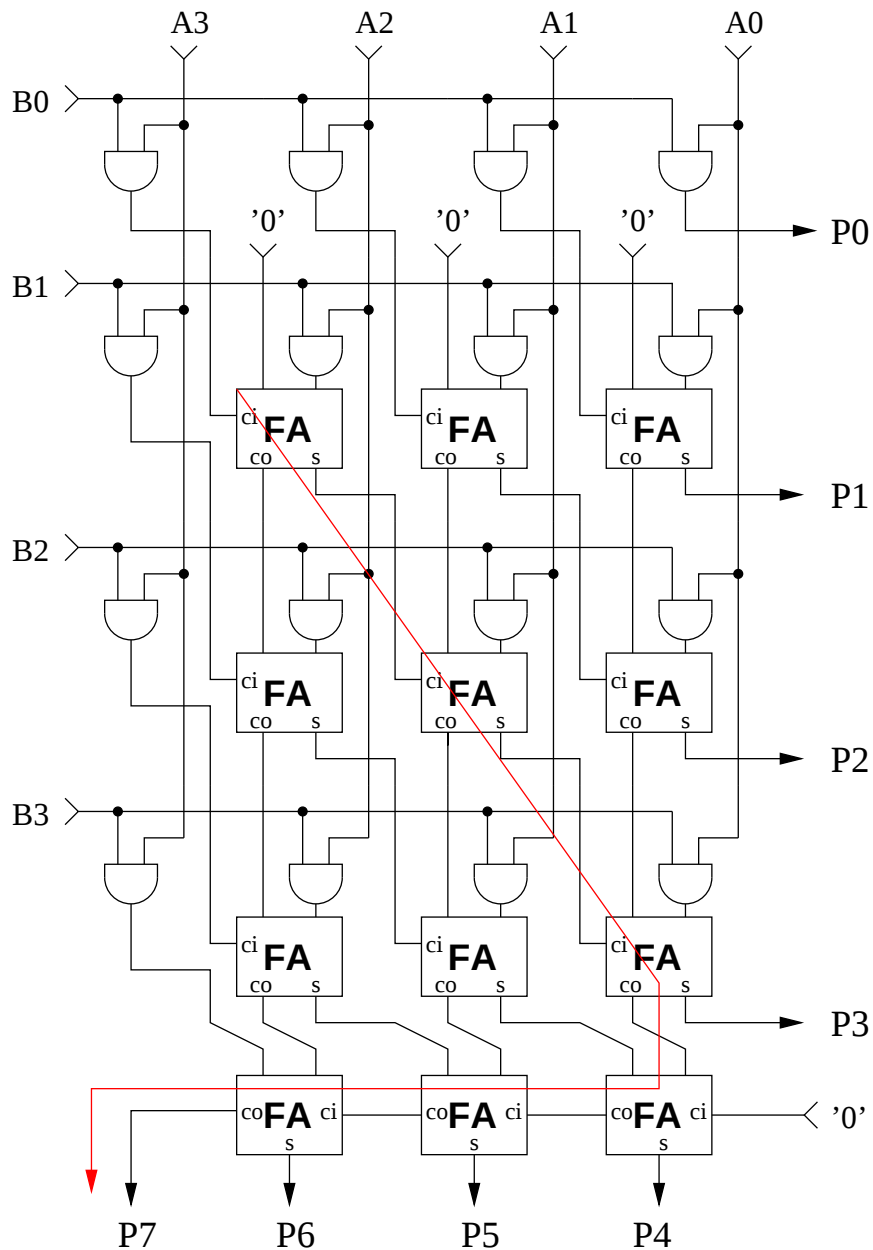


Array Multiplier

Version 2

4x4-Bit Binary Multiplication



Worst Case Propagation Delay: $T_{pd} = [2N-2] \cdot t_{pd}(FA)$