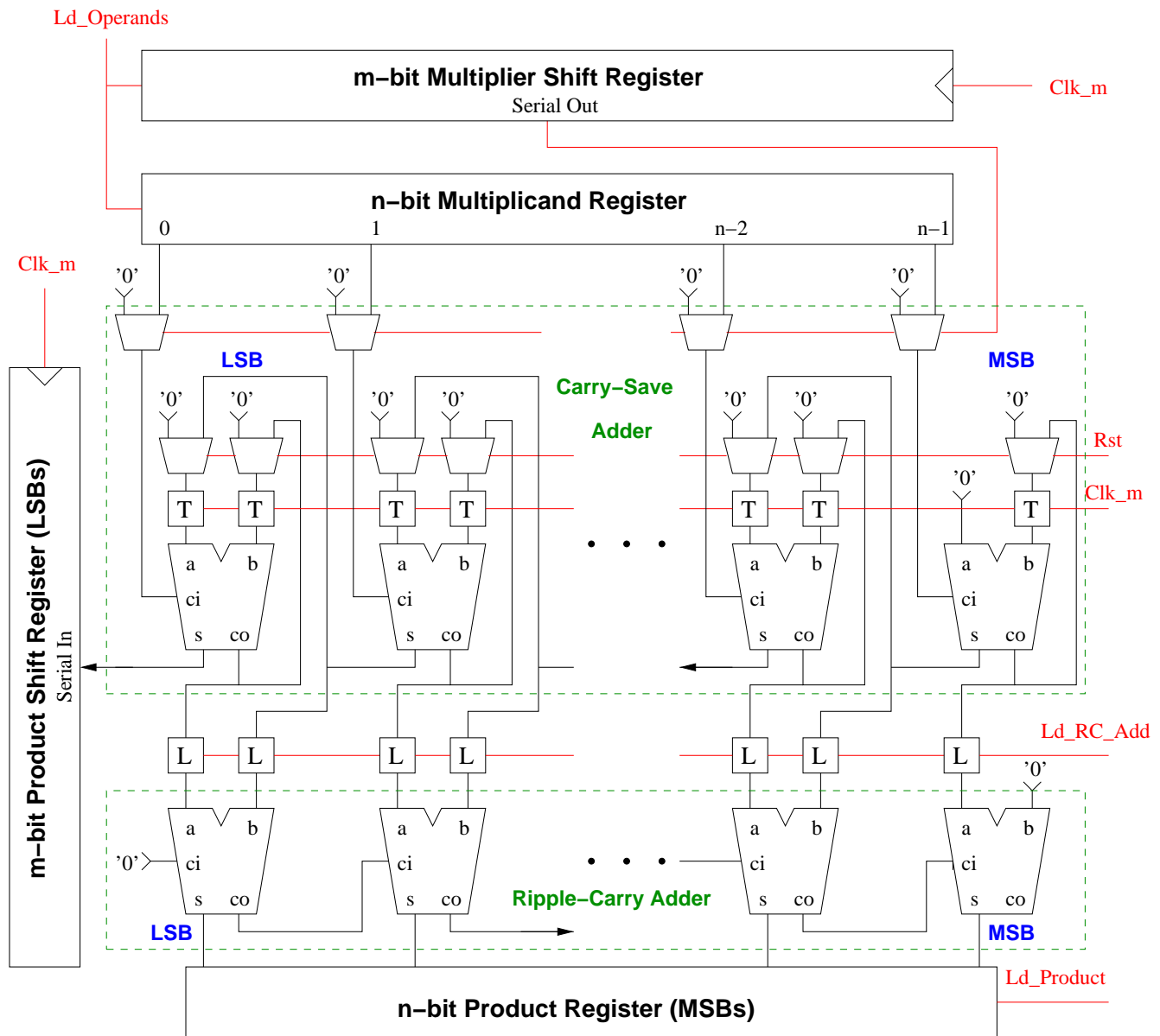


Serial/Parallel Multiplier with Carry-Save Adder

$m \times n$ bit unsigned Multiplier



Latency: $2 \cdot m$ cycles of **Clk_m**

Throughput Rate: $1/m \cdot \text{Clk}_m$