

Engineering Brief  
(*Abbreviated Report Format*)  
10 page maximum

- 1.0 Introduction
- 2.0 Design of the Comb Filter
  - 2.1 Cell design of the integrator/accumulator & differentiator
  - 2.2 Implementation of the decimation operation
  - 2.3 Sign extension (# of bits for 1:8 decimation & 1:16 decimation)
- 3.0 Timing
  - initial timing diagram should be in here
  - 3.1 Clock implementation
  - 3.2 Pulse Generation
- 4.0 Results