

Name:

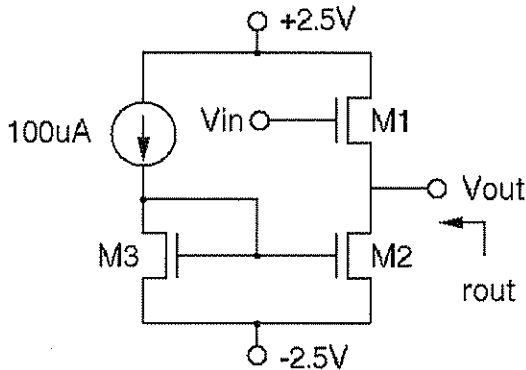
Only solve **three** of the four problems stated!

1) Device Physics

An n-well CMOS process features a p-substrate doping concentrations of $N_A=1.0 \times 10^{23} \text{ m}^{-3}$. Furthermore, we know that $\epsilon_{\text{ox}}=3.6 \times 10^{-11} \text{ As/Vm}$ and $n_i=2.0 \times 10^{16} \text{ m}^{-3}$ at $T=300\text{K}$

- Determine the n-channel Fermi potential ϕ_{Fn} at room temperature.
- The n-channel flat-band voltage is approximately $V_{FBn}=-0.7 \text{ V}$. Calculate the gate oxide thickness t_{ox} such that $V_{\text{tn}}=0.8 \text{ V}$.
- How **much** does the threshold voltage V_{tn} of an n-channel device change if its source is 2V above the substrate potential? To find your answer, use the t_{ox} value listed in the BSIM parameters on page 3.

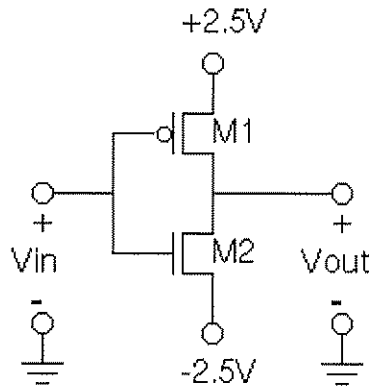
2) Common Drain Amplifier



All transistors have the same W/L ratio of **100/4** and their physical parameters are: $\mu=4 \times 10^{-2} \text{ m}^2/\text{Vs}$, $C_{\text{ox}}=2.5 \times 10^{-3} \text{ F/m}^2$, $\phi_F=0.4 \text{ V}$, $V_{t0}=0.7 \text{ V}$, $\gamma=0.6 \text{ V}^{1/2}$ and $\lambda=0.02 \text{ V}^{-1}$. Assume that V_{in} is generated by an **ideal ac** voltage source.

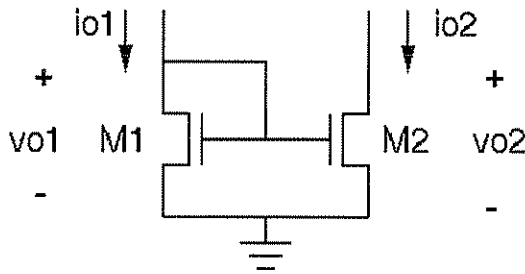
- Find the **dc component** of the output voltage V_{out} if all transistor substrates are connected to -2.5V .
- Find an expression and a numerical value for the output resistance r_{out} .
- Derive an expression for the voltage transfer function $A_v=V_{\text{out}}/V_{\text{in}}$ and find the numerical value for the given transistor parameters.

CMOS Inverter



- Sketch the **small signal linear equivalent circuit** for this amplifier under the assumption that both devices are operated in saturation.
- Derive an expression for the **dc voltage gain** $A_v = V_{out}/V_{in}$ (assume $V_{inQ} = V_{outQ} = 0$). Your final gain expression should contain only **physical device parameters** and pertinent bias voltages. No numerical value is required.
- Determine the 3 dB corner frequency (in Hz) if the circuit drives a capacitive load of $C_L = 500\text{fF}$. Use $(W/L)_1 = 6$, $(W/L)_2 = 2$ and $\lambda_1 = \lambda_2 = 0.02\text{V}^{-1}$.

3) Differential Load



- Sketch the **ac equivalent circuit** for the differential load stage depicted above.
- Assume that the two branches are operated under **common-mode** conditions, where $i_{o1} = i_{o2}$. Find the two equivalent output resistors $r_{o1cm} = v_{o1}/i_{o1}$ and $r_{o2cm} = v_{o2}/i_{o2}$, respectively. Only provide **symbolic** expressions, no numerical values!
- Find the two equivalent output resistors under **differential-mode** conditions, where $i_{o1} = -i_{o2}$. Again, no numerical values are required.

SPICE BSIM3 VERSION 3.1 PARAMETERS

```
.MODEL nfet NMOS (
+VERSION = 3.1          TNOM = 27          LEVEL = 49
+XJ = 1.5E-7           NCH = 1.7E17       TOX = 1.39E-8
+K1 = 0.8857752       K2 = -0.0935679      VTH0 = 0.6398186
+K3B = -7.6711263     WO = 1E-8          K3 = 22.1010569
+DVTOW = 0            DVT1W = 0           NLX = 1E-9
+DVT0 = 2.7950058     DVT1 = 0.4085592    DVT2W = 0
+UO = 453.2010286     UA = 2.494433E-13   DVT2 = -0.1237812
+UC = 2.022743E-11    VSAT = 1.730467E5    UB = 1.488658E-18
+AGS = 0.1151449      B0 = 2.792031E-6     A0 = 0.5543744
+KETA = -1.371458E-3  A1 = 0               B1 = 5E-6
+RDSW = 1.319508E3    PRWG = 0.0381943    A2 = 0.3560219
+WR = 1               WINT = 2.507126E-7  PRWB = 0.0141195
+XL = 0               XW = 0               LINT = 2.304464E-8
+DWB = 4.946821E-8    VOFF = 0             DWG = -1.755808E-8
+CIT = 0              CDSC = 2.4E-4        NFACTOR = 0.7910748
+CDSCB = 0            ETA0 = 0.0051332    CDSCD = 0
+DSUB = 0.1945608     PCLM = 2.253484     ETAB = -1.252309E-3
+PDIBLC2 = 2.440187E-3 PDIBLCB = -0.1294159 PDIBLC1 = -1
+PSCBE1 = 5.348212E8  PSCBE2 = 3.233314E-5 DROUT = 0.6751288
+DELTA = 0.01         RSH = 80.3          PVAG = 0
+PRT = 0              UTE = -1.5          MOBMOD = 1
+KT1L = 0             KT2 = 0.022         KT1 = -0.11
+UB1 = -7.61E-18     UC1 = -5.6E-11     UA1 = 4.31E-9
+WL = 0               WLN = 1             AT = 3.3E4
+WWN = 1              WWL = 0             WW = 0
+LLN = 1              LW = 0              LL = 0
+LWL = 0              CAPMOD = 2          LWN = 1
+CGDO = 2.12E-10      CGSO = 2.12E-10     XPART = 0.5
+CJ = 4.279445E-4     PB = 0.9616445      CGBO = 1E-9
+CJSW = 3.492439E-10 PBSW = 0.1           MJ = 0.4374524
+CJSWG = 1.64E-10     PBSWG = 0.1         MJSW = 0.1245165
+CF = 0               PVTH0 = 0.0431719  MJSWG = 0.1245165
+PK2 = -0.0350028     WKETA = -0.0230093 PRDSW = -30.376525
*                      *                      LKETA = 2.090253E-3)
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```
.MODEL pfet PMOS (
+VERSION = 3.1          TNOM = 27          LEVEL = 49
+XJ = 1.5E-7           NCH = 1.7E17       TOX = 1.39E-8
+K1 = 0.5429357       K2 = 9.433657E-3    VTH0 = -0.9488171
+K3B = -0.8567156     WO = 1E-8          K3 = 3.2656684
+DVTOW = 0            DVT1W = 0           NLX = 1.48542E-8
+DVT0 = 2.530444      DVT1 = 0.5291909    DVT2W = 0
+UO = 220.9301068     UA = 3.049951E-9    DVT2 = -0.1040273
+UC = -5.63429E-11    VSAT = 2E5          UB = 1E-21
+AGS = 0.1506017      B0 = 9.121548E-7    A0 = 0.9085767
+KETA = -2.819843E-3  A1 = 0               B1 = 5E-6
+RDSW = 3E3           PRWG = -0.0464229   A2 = 0.3
+WR = 1               WINT = 2.90101E-7  PRWB = -0.0398483
+XL = 0               XW = 0               LINT = 4.254314E-8
+DWB = 1.788287E-8    VOFF = -0.0659109  DWG = -2.169468E-8
+CIT = 0              CDSC = 2.4E-4        NFACTOR = 0.8188201
+CDSCB = 0            ETA0 = 1.380153E-3  CDSCD = 0
+DSUB = 0.7658995     PCLM = 2.0797597    ETAB = -0.0429727
+PDIBLC2 = 4.521707E-3 PDIBLCB = -0.0437905 PDIBLC1 = 0.1113965
+PSCBE1 = 1.25116E10  PSCBE2 = 1.227353E-9 DROUT = 0.3065171
+DELTA = 0.01         RSH = 104.9         PVAG = 8.477076E-6
+PRT = 0              UTE = -1.5          MOBMOD = 1
+KT1L = 0             KT2 = 0.022         KT1 = -0.11
+UB1 = -7.61E-18     UC1 = -5.6E-11     UA1 = 4.31E-9
+WL = 0               WLN = 1             AT = 3.3E4
+WWN = 1              WWL = 0             WW = 0
+LLN = 1              LW = 0              LL = 0
+LWL = 0              CAPMOD = 2          LWN = 1
+CGDO = 2.25E-10      CGSO = 2.25E-10     XPART = 0.5
+CJ = 7.308538E-4     PB = 0.9416073      CGBO = 1E-9
+CJSW = 2.852637E-10 PBSW = 0.99          MJ = 0.4948413
+CJSWG = 6.4E-11      PBSWG = 0.99        MJSW = 0.3001719
+CF = 0               PVTH0 = 5.98016E-3 MJSWG = 0.3001719
+PK2 = 3.73981E-3     WKETA = 4.127712E-3 PRDSW = 14.8598424
*                      *                      LKETA = -2.567864E-3)
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1) a) $\| \Phi_n = \frac{kT}{q} \ln\left(\frac{N_{sub}}{n_i}\right) \approx 400 \text{ mV} \|$

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b) $V_t = V_{FIS} + 2\Phi_n + \frac{t_{ox}}{\epsilon_{ox}} \sqrt{2\epsilon_s 2q_n q N_{sub}}$

$\therefore \| t_{ox} = \frac{\epsilon_{ox}}{\sqrt{2\epsilon_s 2q_n q N_{sub}}} (V_t - V_{FIS} - 2\Phi_n) \approx 18 \text{ nm} \|$

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c) $V_t(V_{SIS}) = V_{t0} + \eta [\sqrt{2q_F + V_{SIS}} - \sqrt{2q_F}]$

where $|\eta = \frac{t_{ox}}{\epsilon_{ox}} \sqrt{2\epsilon_s q N_{sub}} \approx 0.69 \text{ V}^{-1}|$

$\therefore \| V_t(V_{SIS} = 2V) = 1.34 \text{ V} \|$

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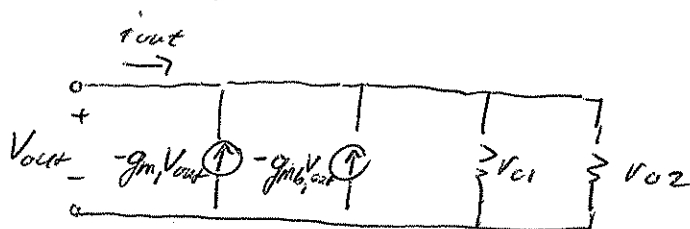
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$$2) a) V_{out,dc} = -V_t - \sqrt{\frac{2I_{DQ}}{\mu C_{ox} W/L}} \quad I_{DQ} = 100 \mu A$$

$$\text{where } V_t = V_{t0} + \gamma [\sqrt{2\phi_F + V_{SB}} - \sqrt{2\phi_F}] \approx 1.0V$$

$$\therefore \left\| V_{out,dc} = -1.0V - \sqrt{\frac{2I_{DQ}}{\mu C_{ox} W/L}} \approx -1.3V \right\|$$

b) linear eq. circuit to determine τ_{out}

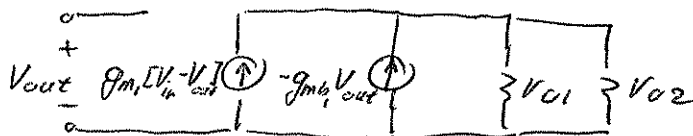


$$\left. \begin{aligned} r_{o1} = r_{o2} = 500k\Omega \\ f_{m1} = 0.71 \text{ mS} \\ g_{mb1} = f_{m1} \cdot 0.21 \end{aligned} \right|$$

$$\therefore V_{out} = [i_{out} - (f_{m1} + f_{mb1})V_{out}] \tilde{r}_o \quad \tilde{r}_o = r_{o1} \parallel r_{o2}$$

$$\left\| \tau_{out} = \frac{V_{out}}{i_{out}} = \frac{\tilde{r}_o}{1 + (f_{m1} + f_{mb1})\tilde{r}_o} \approx \frac{1}{f_{m1} + f_{mb1}} \approx 1.2 \mu s \right\|$$

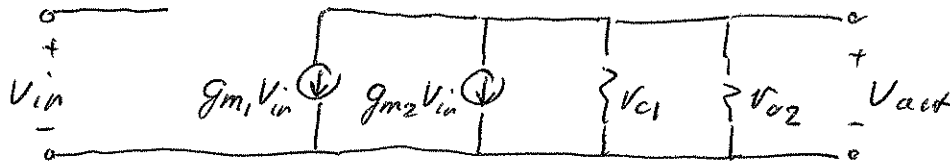
c) linear eq. circuit to determine voltage transfer function



$$\therefore V_{out} = g_{m1} \tilde{r}_o [V_{in} - V_{out}] - g_{mb1} \tilde{r}_o V_{out}$$

$$\left\| A_v = \frac{V_{out}}{V_{in}} = \frac{g_{m1} \tilde{r}_o}{1 + g_{m1} \tilde{r}_o + g_{mb1} \tilde{r}_o} \approx \frac{g_{m1}}{g_{m1} + g_{mb1}} \approx 0.83 \right\|$$

3) a)



$$b) \quad |A_v = \frac{V_{out}}{V_{in}} = - (g_{m1} + g_{m2}) r_{O1} \parallel r_{O2}| \quad \text{Z}$$

$$g_m = \mu C_{ox} \frac{W}{L} [V_{DD} - V_t] \quad r_o = \frac{2}{\lambda \mu C_{ox} \frac{W}{L} [V_{DD} - V_t]^2}$$

$$\therefore |A_v| \approx - \frac{2 [(\mu C_{ox} \frac{W}{L})_1 + (\mu C_{ox} \frac{W}{L})_2]}{[V_{DD} - V_t] [(\lambda \mu C_{ox} \frac{W}{L})_1 + (\lambda \mu C_{ox} \frac{W}{L})_2]} \quad \parallel \quad |V_{t1}| \approx |V_{t2}|$$

If $\lambda_1 \approx \lambda_2$ and $(\mu C_{ox} \frac{W}{L})_1 \approx (\mu C_{ox} \frac{W}{L})_2$

$$|A_v| \approx - \frac{2}{[V_{DD} - V_t] \lambda} \quad \text{Z}$$

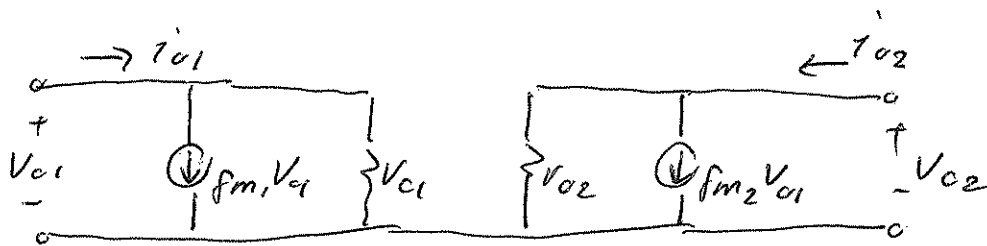
c) Corner frequency due to C_L

$$|\omega_p = \frac{1}{\tilde{r}_o C_L}|$$

$$|\tilde{r}_o = \frac{2}{[(\lambda \mu C_{ox} \frac{W}{L})_1 + (\lambda \mu C_{ox} \frac{W}{L})_2] [V_{DD} - V_t]^2} \approx 56 \text{ k}\Omega|$$

$$\therefore |f_c = \frac{1}{2\pi} \frac{1}{\tilde{r}_o C_L} \approx 5.7 \text{ MHz}| \quad \text{Z}$$

4) a)

b) common mode $\therefore i'_{o1} = i'_{o2}$

$$\begin{cases} V_{o1} = (i'_{o1} - g_{m1}V_{o1})r_{o1} \\ V_{o2} = (i'_{o2} - g_{m2}V_{o1})r_{o2} \end{cases} \quad \therefore V_{o1} = i'_{o1} \frac{r_{o1}}{1 + g_{m1}r_{o1}}$$

$$\therefore V_{o2} = \left(i'_{o2} - i'_{o2} \frac{g_{m2}r_{o1}}{1 + g_{m1}r_{o1}} \right) r_{o2}$$

$$\left\| \begin{aligned} r_{o1cm} &= \frac{r_{o1}}{1 + g_{m1}r_{o1}} \approx \frac{1}{g_{m1}} \\ r_{o2cm} &= \frac{r_{o2}}{1 + g_{m1}r_{o1}} \approx \frac{1}{g_{m1}} \end{aligned} \right\|$$

c) differential mode $i'_{o1} = -i'_{o2}$

$$\therefore V_{o2} = \left(i'_{o2} + i'_{o2} \frac{g_{m2}r_{o1}}{1 + g_{m1}r_{o1}} \right) r_{o2}$$

$$\left\| r_{o1dm} = r_{o1cm} \approx \frac{1}{g_{m1}} \right\|$$

$$\left\| r_{o2dm} = \frac{1 + (g_{m1} + g_{m2})r_{o1}}{1 + g_{m1}r_{o1}} \approx 2r_{o1} \right\|$$