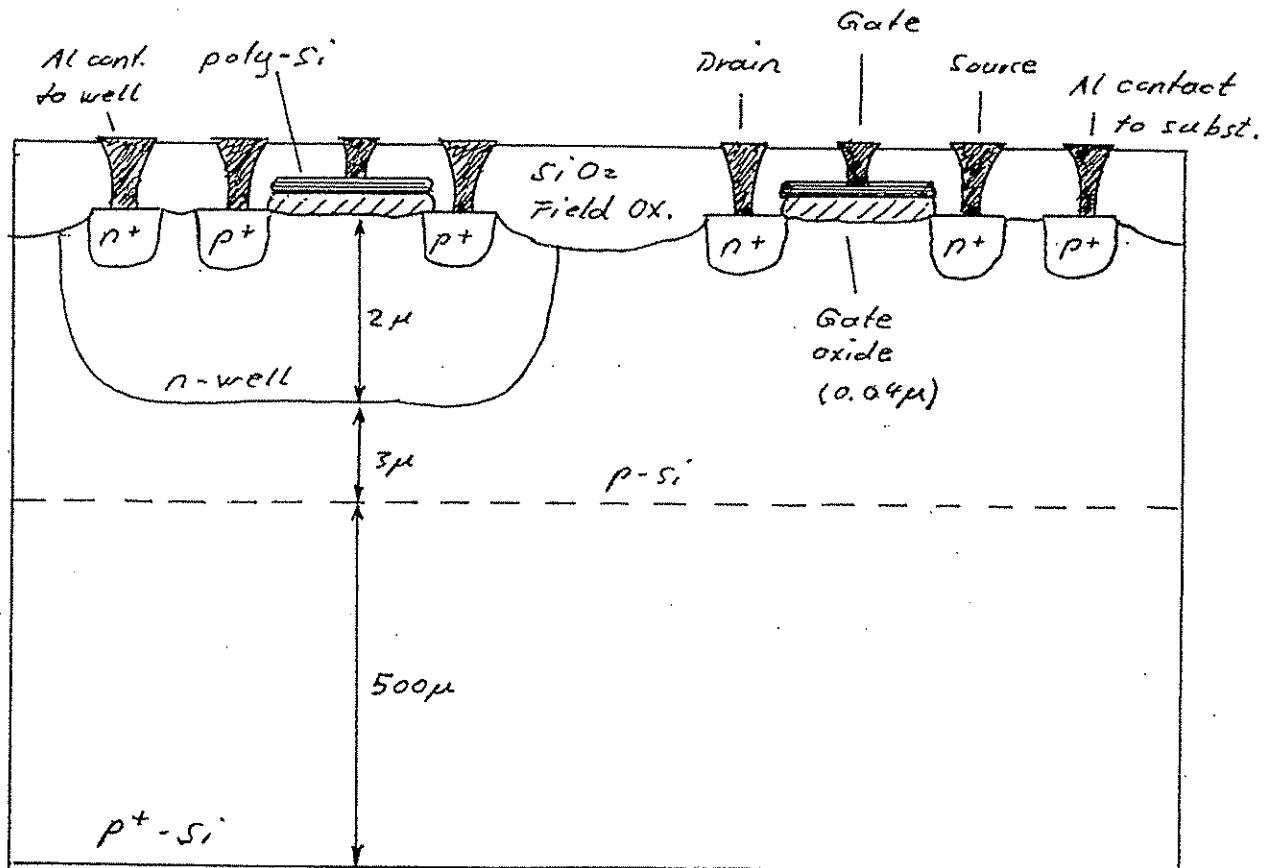
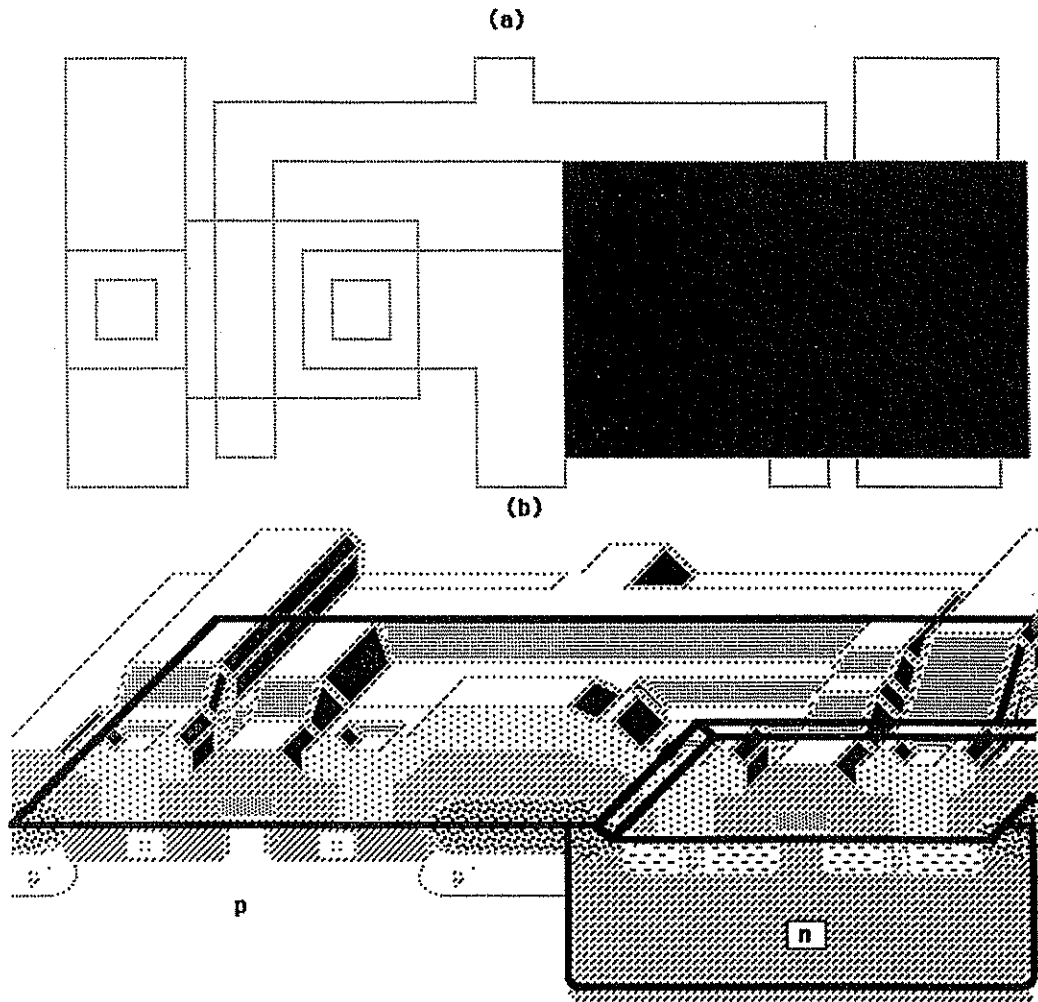


CMOS Technology - Overview

CMOS Transistor Pair (n-well process)

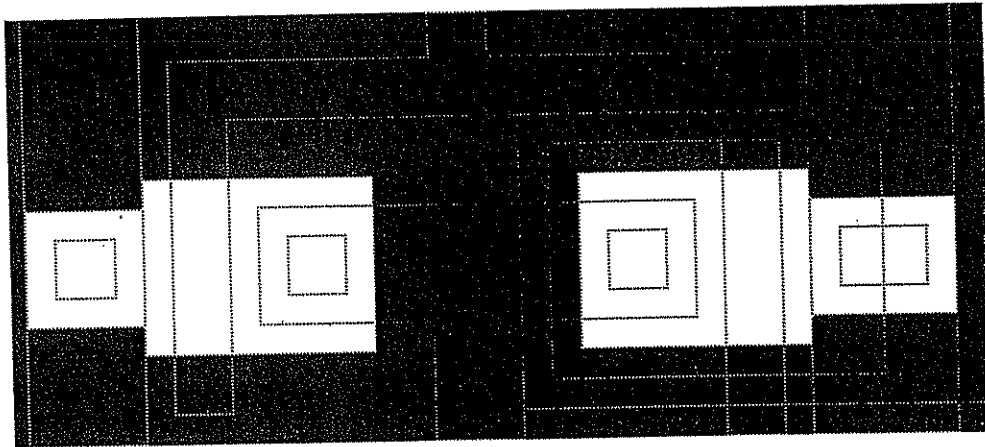


N-WELL CMOS TECHNOLOGY

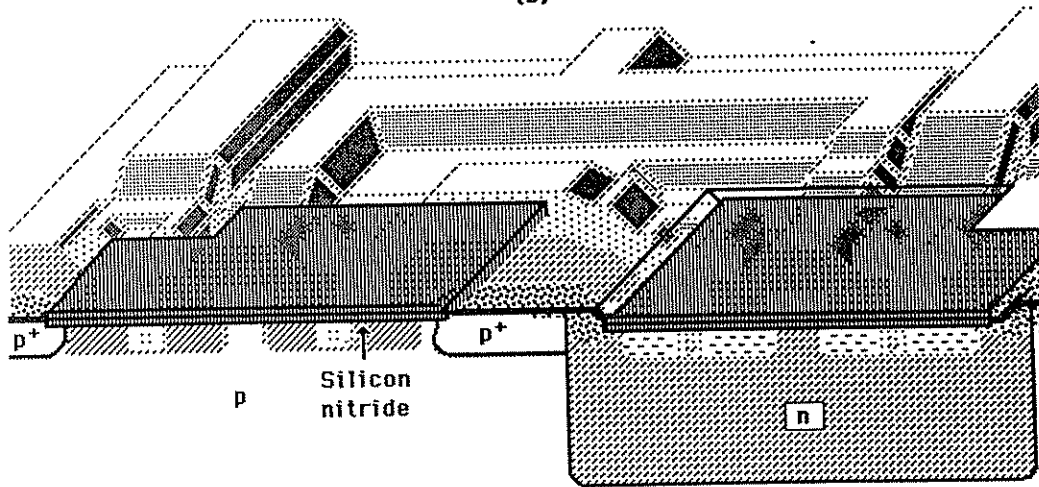


N-WELL CMOS TECHNOLOGY

(a)

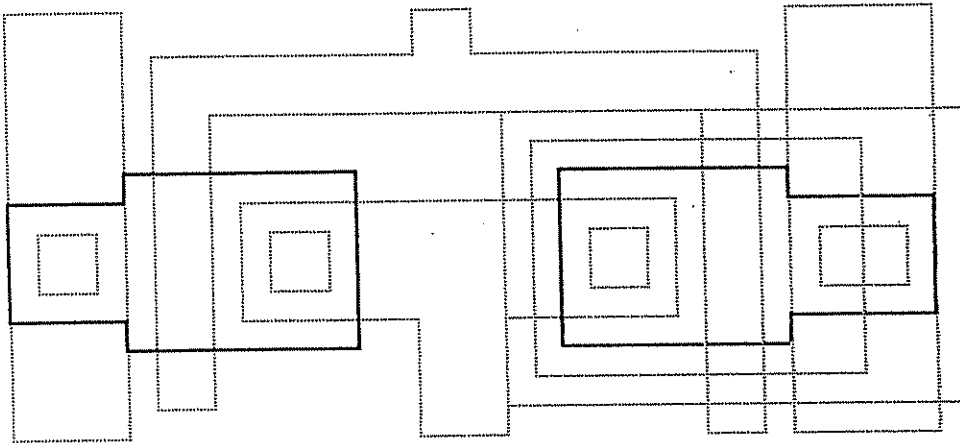


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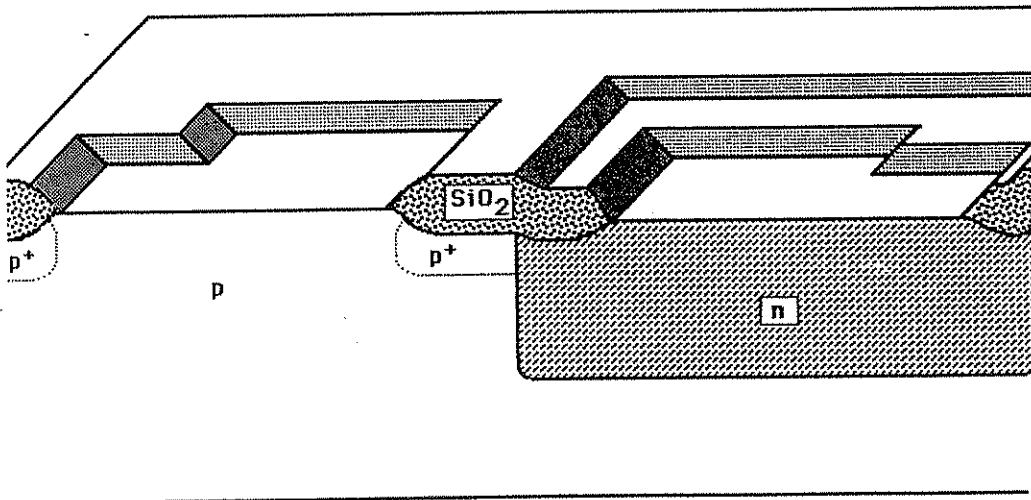


N-WELL CMOS TECHNOLOGY

(a)

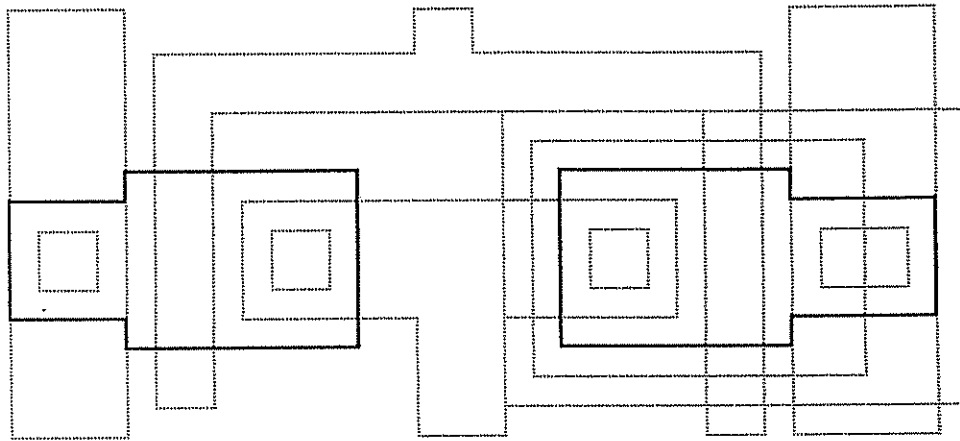


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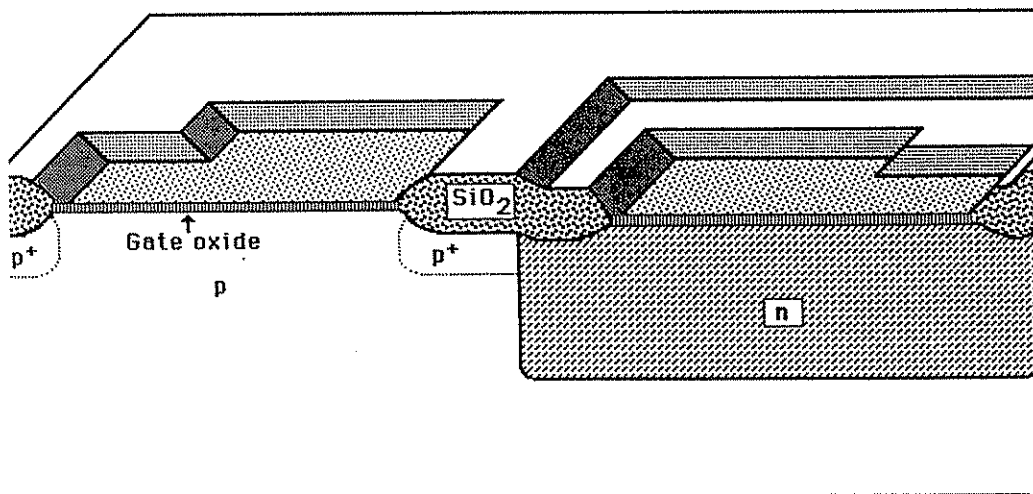


N-WELL CMOS TECHNOLOGY

(a)

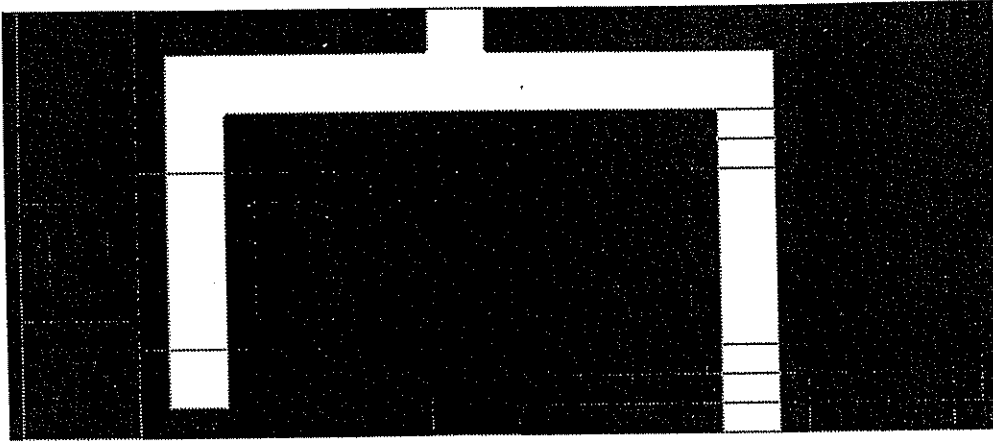


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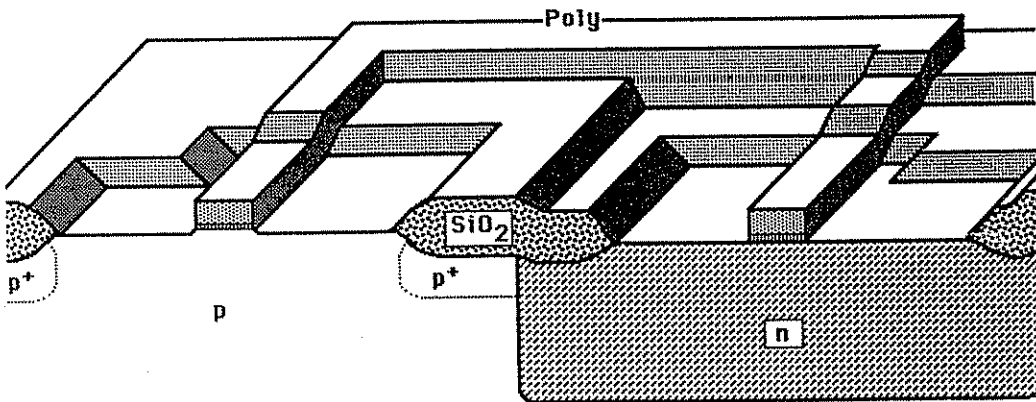


N-WELL CMOS TECHNOLOGY

(a)

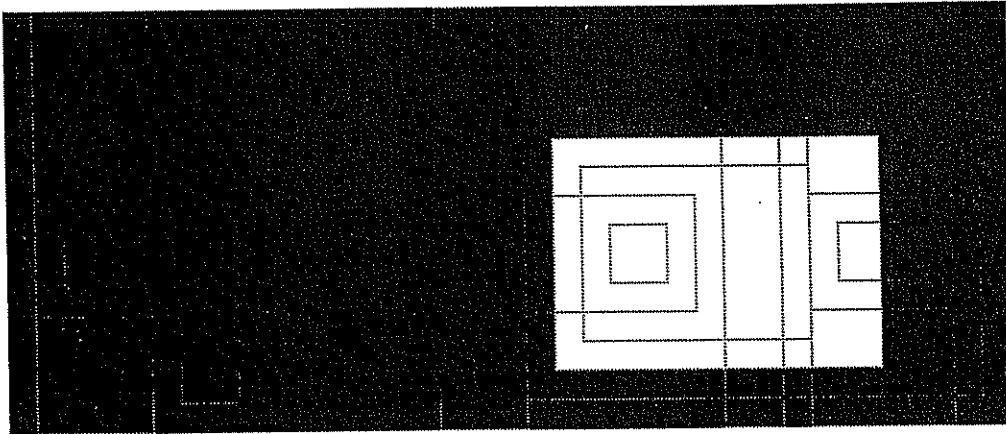


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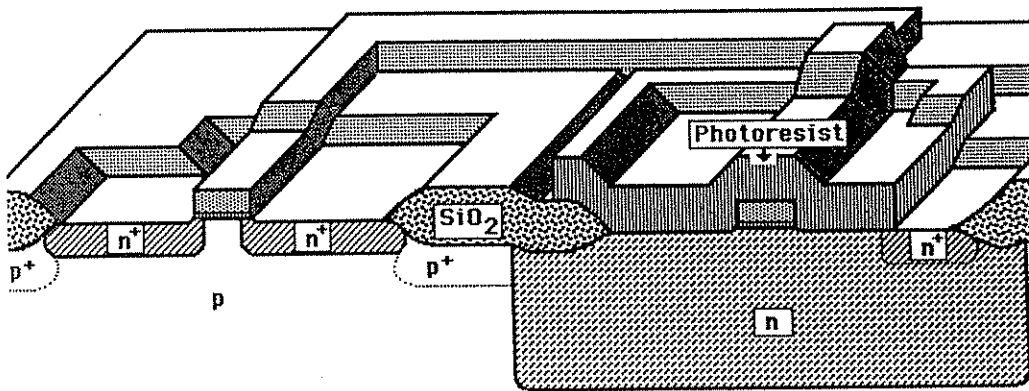


N-WELL CMOS TECHNOLOGY

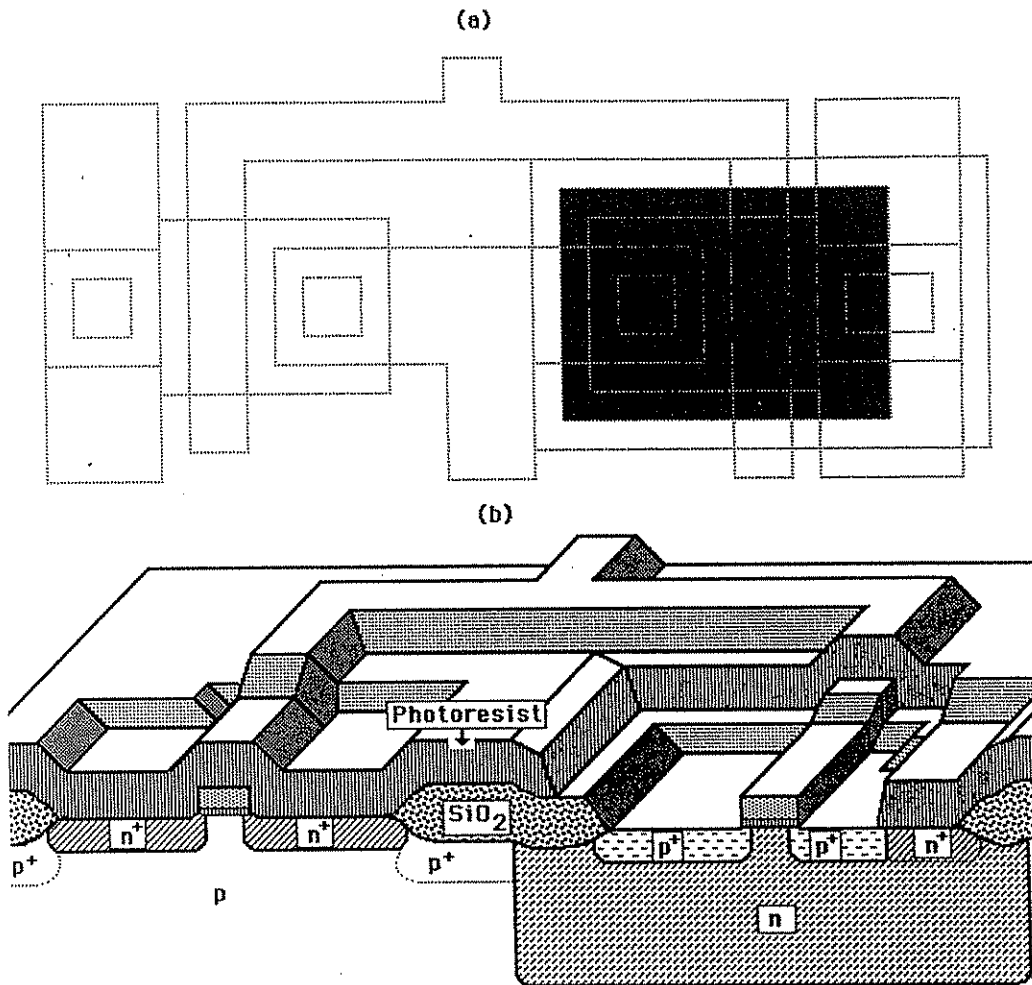
(a)



(b)

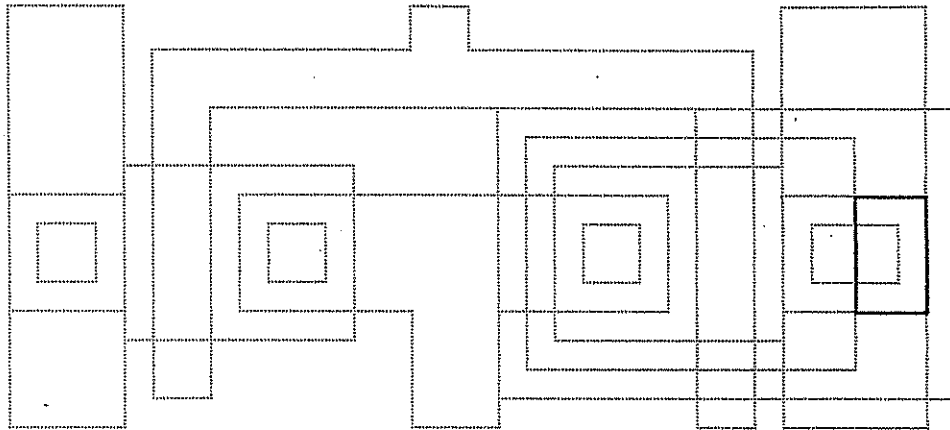


N-WELL CMOS TECHNOLOGY

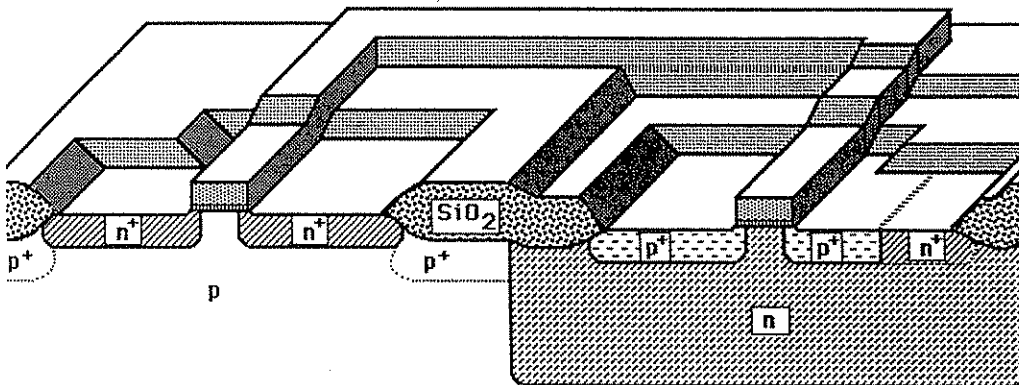


N-WELL CMOS TECHNOLOGY

(a).

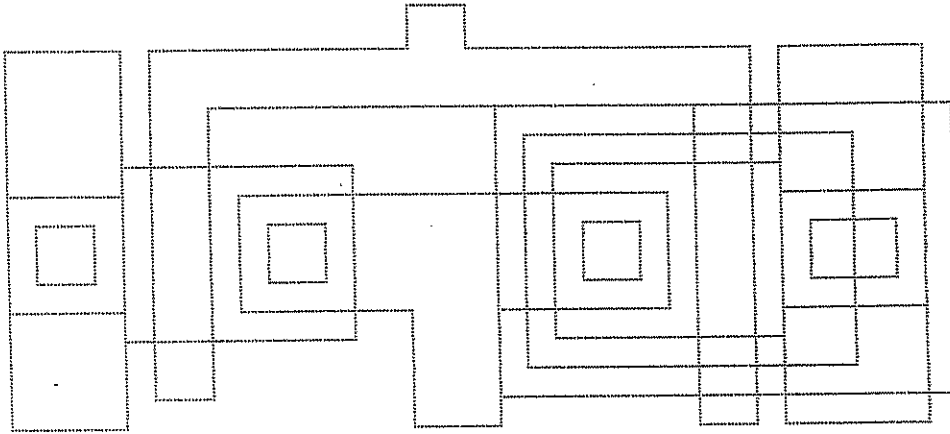


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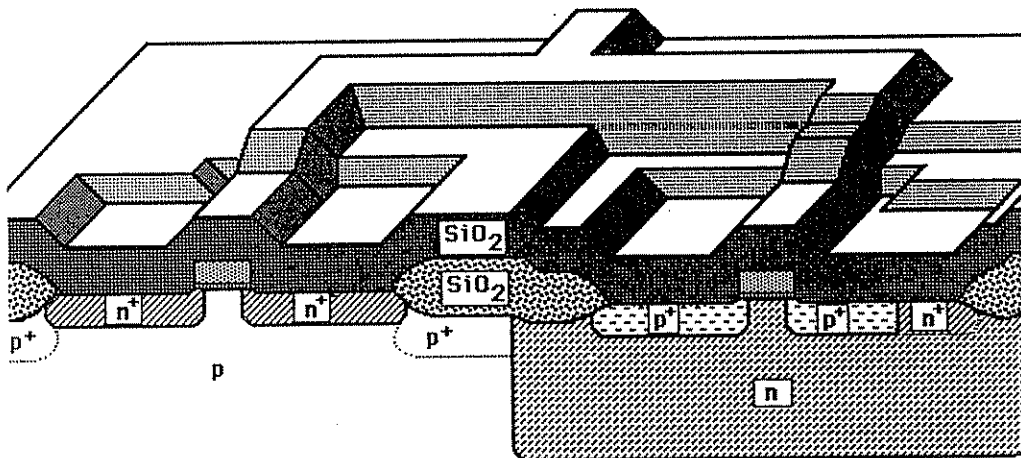


N-WELL CMOS TECHNOLOGY

(a)

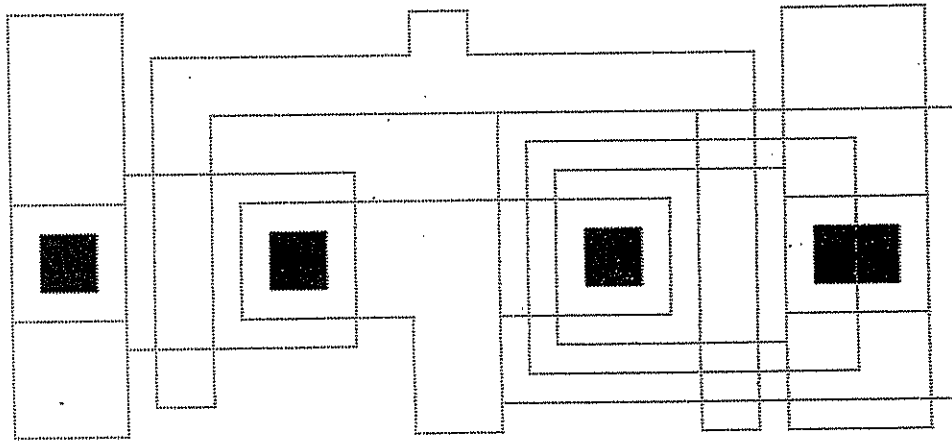


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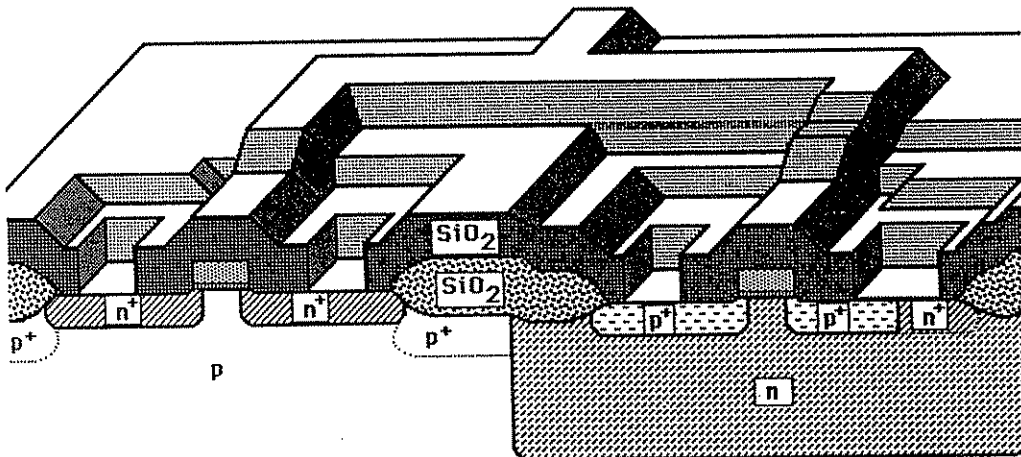


N-WELL CMOS TECHNOLOGY

(a)

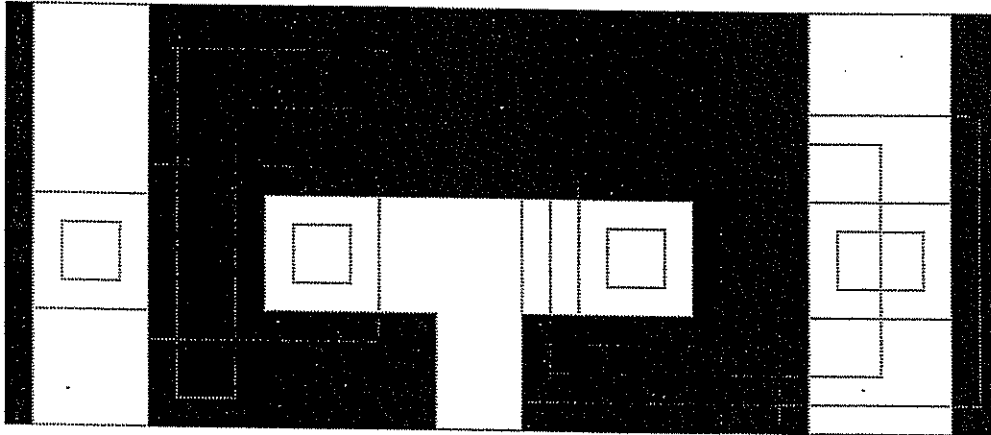


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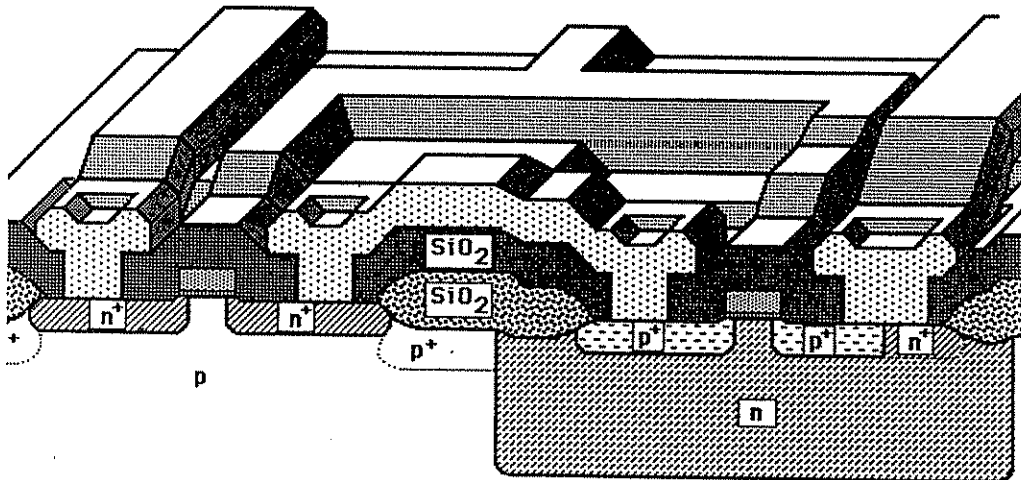


N-WELL CMOS TECHNOLOGY

(a)



(b)



N-WELL CMOS TECHNOLOGY

