

Qing Yang is Distinguished Engineering Professor in the Department of Electrical, Computer, and Biomedical Engineering at University of Rhode Island where he has been a faculty member since 1988. He is a recipient of 8 accomplishment awards while serving at URI such as Faculty Excellence Award, Distinguished Engineering Professor Award, Outstanding Intellectual Property Award.

His research interests include computer architectures, memory and storage systems, computer security, embedded computer systems and applications. He has published over 100 high quality technical articles in these research fields and held over two dozens issued patents. Majority of his patents have been licensed to computer industry with significant practical impact. Four high tech startup companies have been formed based on his patents. His latest startup, [VeloBit](#), was based on his newly proposed concept: Content Locality, and was successfully [acquired by Western Digital in July 2013](#).

He has graduated 11 PhD students, of whom 4 are faculty members at major universities and others are leading researchers in computer companies such as Intel, Xerox, and EMC.

Yang is a Fellow of IEEE. He has served in the professional society in various capacities including general chair of the ACM/IEEE International Symposium on Computer Architecture (ISCA2011), IEEE international Conference on Network, Architecture, and Storage (NAS); IEEE Distinguished Speaker; Editor of IEEE Transactions; and Program Committee member of numerous international conferences.

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